

REDUCING THE BIAS ON SILICON LIGHT MODULATORS

Abstract of the Disclosure

A negative bias voltage may be utilized to bias a spatial light modulator and to enable the spatial light
5 modulator to be modulated using relatively low supply voltages. During the negative frame, a positive bias voltage may be utilized and during the positive frame, a negative bias voltage may be utilized. This avoids damage to the liquid crystal material while reducing the necessary
10 modulating voltages to within the range available from leading edge silicon technologies.